This packet of four learning modules on computer usage is one of eight such packets developed for apprenticeship training for low voltage alarm. Introductory materials are a complete listing of all available modules and a supplementary reference list. Each module contains some or all of these components: goal, performance indicators, study guide (a check list of steps the student should complete), a vocabulary list, an introduction, information sheets, assignment sheet, job sheet, self-assessment, self-assessment answers, post-assessment, instructor post-assessment answers, and a list of supplementary references. Supplementary reference material may be provided. The four training modules cover digital language, digital logic, computer overview, and computer software. (YLS)
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APPRENTICESHIP

LOW VOLTAGE ALARM
RELATED TRAINING MODULES

0.1 History of Alarms

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1.1 Linear - Measurement
1.2 Whole Numbers
1.3 Addition and Subtraction of Common Fractions and Mixed Numbers
1.4 Multiplication and Division of Common Fractions and Mixed Numbers
1.5 Compound Numbers
1.6 Percent
1.7 Mathematical Formulas
1.8 Ratio and Proportion
1.9 Perimeters, Areas and Volumes
1.10 Circumference and Area of Circles
1.11 Areas of Planes, Figures, and Volumes of Solid Figures
1.12 Graphs
1.13 Basic Trigonometry
1.14 Metrics

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2.1 Basics of Energy
2.2 Atomic Theory
2.3 Electrical Conduction
2.4 Basics of Direct Current
2.5 Introduction to Circuits
2.6 Reading Scales
2.7 Using a V.O.M.
2.8 OHM'S Law
2.9 Power and Watt's Law
2.10 Kirchoff's Current Law
2.11 Kirchoff's Voltage Law
2.12 Series Resistive Circuits
2.13 Parallel Resistive Circuits
2.14 Series - Parallel Resistive Circuits
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2.17 Magnetism
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3.3 Schematics and Alarm Design
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4.2 Charging Circuits
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4.4 Fuse and Circuit Breaker Protection
4.5 Battery Standby Capacity
4.6 Batteries
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5.2 Troubleshooting - Environmental Factors
5.3 Documentation of Design
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6.2 Hand Tool Safety
6.3 Power Tool Safety
6.4 Fire Safety
6.5 Hygiene Safety
6.6 Safety and Electricity

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7.2 Theory of Bi-polar Devices
7.3 Theory of Integrated Circuits
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8.2 Logic Gates
8.3 Dialers
9.1 Blueprint Reading, Building Materials and Symbols
9.2 Design of Alarm Systems
10.1 Types and Applications of Alarm Systems
10.2 Burglar Systems
10.3 Fire Alarms
10.4 Hold-up Alarm Systems
10.5 Bank Alarm Systems
10.6 Wireless Alarm Systems
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11.2 Maintain Hand and Power Tools
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12.1 Photoelectric Space Detectors
12.2 Passive Infrared Motion Detectors
12.3 Microwave Detectors (Radar)
12.4 Stress Detectors in Space and Volumetric Applications
12.5 Capacitance Detectors
12.6 Sound Discrimination
12.7 Ultrasonic Motion Detectors
12.8 Gas Detectors
12.9 Airborne and Structural Problems
12.10 Audio Detection Systems
13.1 Trade Terms
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14.2 Fence Disturbance Sensors
14.3 Electric Field Sensors
14.4 Seismic Sensors
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15.1 Annunciators
15.2 Fire Extinguishing Systems
15.3 Signal Reporting Systems
16.1 Detection Devices
16.2 Contacts
16.3 Volumetric and Space Devices
16.4 Problems and Applications of Devices
17.1 Key Stations
17.2 Keyless Control Stations
17.3 Types of Annunciation
17.4 Shunt Switches
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19.1 Builder Board Requirements
19.2 Licensing
20.1 Central Stations
20.2 Fire Department Monitoring
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20.4 Telephone Answering Service Monitoring
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22.1 Card Access Control
22.2 Telephone Access Control
22.3 Computerized Controls and Interfaces
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23.1 Telephone Services
24.1 Basic Sound Systems
25.1 Business Letters
26.1 Video Surveillance Systems
26.2 CCTV Cameras
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26.4 CCTV Monitors and Recorders
26.5 Time-Lapse Video Recorders and Videotape
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28.12 Personal Finance

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29.2 Blueprint Reading/Working Drawings
29.3 Scaling and Dimensioning
29.4 Sketching
29.5 Machine and Welding Symbols
LOW VOLTAGE ALARM
SUPPLEMENTARY REFERENCE MATERIAL

Intrusion Detection Systems: Principles of Operation and Application

Author: Robert L. Barnard
Edition: 1981

Understanding and Servicing Alarm Systems

Author: H. William Trimmer
Edition: 1981

In the event additional copies are needed, they may be purchased through:

Butterworth Publishers
10 Tower Office Park
Woburg, Ma. 01801
RECOMMENDATIONS FOR USING TRAINING MODULES

The following pages list modules and their corresponding numbers for this particular apprenticeship trade. As related training classroom hours vary for different reasons throughout the state, we recommend that the individual apprenticeship committees divide the total packets to fit their individual class schedules.

There are over 130 modules available. Apprentices can complete the whole set by the end of their indentured apprenticeships. Some apprentices may already have knowledge and skills that are covered in particular modules. In those cases, perhaps credit could be granted for those subjects, allowing apprentices to advance to the remaining modules.

We suggest the apprenticeship instructors assign the modules in numerical order to make this learning tool most effective.
Goal:

The apprentice will be able to describe digital language.

Performance Indicators:

1. Identify numbers in decimal, binary, octal and hexadecimal systems.
2. Convert to decimal numbers.
3. Convert to binary and hexadecimal numbers.
4. Convert to binary and octal numbers.
BASIC ELECTRONICS

Digital Language
EL-BE-54

Test Draft
September 1981
Acknowledgment

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Title VI—Equal Education and Legal Specialist
Title IX—Associate Superintendent, Educational Program Audit Division, and Equal Education and Legal Specialist
Section 504—Specialist for Speech, Language and Hearing, Special Education Section

Inquiries may be addressed to the Oregon Department of Education, 700 Pringle Parkway SE, Salem 97310 or to the Regional Office for Civil Rights, Region X, 4321 Second Avenue, Seattle 98101.
Objectives

Given:

A mixed group of numbers.

A group of binary numbers between zero and 16.

A group of octal numbers.

A group of hexadecimal numbers.

Directions

Obtain the following:

Paper and pencil

Learning Activities

_____ Read Key Words list.

_____ Read Information Sheets.

_____ Do Self-Tests.

_____ Complete Final Test.

_____ Obtain Final Evaluation.

The student will:

Identify numbers in decimal, binary, octal and hexadecimal.

Convert to decimal.

Convert to binary and hexadecimal.

Convert to binary and octal.
Key Words

Instruction: That part of a computer program that tells the central processing unit how to manipulate data.

Data: The variable-information that is stored, manipulated, and processed by a computer.

Binary number system: The positional notation number system with two as its base.

Decimal number system: The positional notation number system with ten as its base.

Decimal point: A symbol (.) used in the decimal system that separates whole numbers on the left with 1/10 unit fractions on the right.

Binary point: A symbol (.) used in the binary system that separates whole numbers on the left with 1/2 unit fractions on the right.

Radix: Also called the base. The total number of characters in a number system. The number by which a digit must be multiplied in each place of positional notation. Example: In decimal, each column to the left must be multiplied by ten.

Base: See radix.

Power: (or exponent). The number above and to the right of another number, like $5^2$, that tells how many times it should be used in multiplying by itself.

$5^2 = 5 \times 5 = 25$.

Bit: Binary digit. A single column of binary used in a computer as a single machine language character. There are usually eight bits to a byte.

Octal number system: The positional notation number system with eight as its base.

Hexadecimal number system: The positional notation number system with sixteen as its base.
OVERVIEW

Computers use the binary number system as their language. When a computer screen displays an elaborate picture or when a printer prints out the answer to a complex scientific problem, all of the processing has been done in binary. You will learn about binary in this package. Octal and hexadecimal are merely variations on the binary system that will help people communicate with computers.
Electronic digital systems employ two-state devices. All information, either instruction or data, is in the form of 0's and 1's. The binary system of counting is essential in organizing this information.

The number system we are most used to is the decimal system. The decimal system is based on the number 10. It has ten characters: 0, 1, 2, 3, 4, 5, 6, 7, 8, 9. Quantities are represented by these characters and the column which they are in.

These columns increase in weight to the left of the decimal point by a power of ten. That is to say, when you exceed the capacity of the "ones" column, you must then make use of the column to its left, which is ten times greater or the "tens" column. When you run out of characters in those two columns at "99," you must spill over into the next column where each character represents "100" or ten to the second power ($10^2$). This is called "positional notation."

The same is true of the binary system except that you only use two characters: zero and 1. Each column increases in weight to the left of the binary point by a power of two.

<table>
<thead>
<tr>
<th>Columns (Characters)</th>
<th>Fours</th>
<th>Twos</th>
<th>Ones</th>
</tr>
</thead>
<tbody>
<tr>
<td>1 0 1</td>
<td>1</td>
<td>0</td>
<td>1</td>
</tr>
</tbody>
</table>

1 x 4 0 x 2 1 x 1

Following is an example of characters from 0-15 in decimal and binary:

<table>
<thead>
<tr>
<th>Decimal</th>
<th>2^3</th>
<th>2^2</th>
<th>2^1</th>
<th>1s column</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>0</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>1</td>
<td>1</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>2</td>
<td>0</td>
<td>1</td>
<td></td>
<td></td>
</tr>
<tr>
<td>3</td>
<td>0</td>
<td>0</td>
<td>1</td>
<td></td>
</tr>
<tr>
<td>4</td>
<td>1</td>
<td></td>
<td>1</td>
<td>0</td>
</tr>
<tr>
<td>5</td>
<td>1</td>
<td></td>
<td>1</td>
<td>0</td>
</tr>
<tr>
<td>6</td>
<td>1</td>
<td></td>
<td>1</td>
<td>1</td>
</tr>
<tr>
<td>7</td>
<td>1</td>
<td></td>
<td>1</td>
<td>1</td>
</tr>
<tr>
<td>8</td>
<td>1</td>
<td>0</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>9</td>
<td>1</td>
<td>0</td>
<td>0</td>
<td>1</td>
</tr>
<tr>
<td>10</td>
<td>1</td>
<td>0</td>
<td>1</td>
<td>0</td>
</tr>
<tr>
<td>11</td>
<td>1</td>
<td>0</td>
<td>1</td>
<td>1</td>
</tr>
<tr>
<td>12</td>
<td>1</td>
<td>1</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>13</td>
<td>1</td>
<td>1</td>
<td>0</td>
<td>1</td>
</tr>
<tr>
<td>14</td>
<td>1</td>
<td>1</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>15</td>
<td>1</td>
<td>1</td>
<td>1</td>
<td>1</td>
</tr>
</tbody>
</table>
Notice the similarities and differences between the two systems of numbering:
Base 10 - decimal and Base 2 - binary.

1. There are 10 characters in Decimal 0-9: There are 2 characters in binary 0 and 1.

2. The base (or-radix) does not appear in this set of characters.

3. There is only one character in each column.

4. Each column is greater by the "base power" than the column to its right, and smaller by the "base power" than the column to its left.

Example:

Decimal: 6 3 210

The subscript (10) tells the base (if not present, it is generally assumed to be 10).

The 3 represents a value 10 times greater than a character in the column to its right and 10 times less than one in the column to its left.

Binary: 1 1 102

8 + 4 + 2 + 0 = 1410

The 1 in the second column is 2 times greater than the character in the first column (0). Each lateral position to the left increases by a power of 2.

<table>
<thead>
<tr>
<th>2^5</th>
<th>2^4</th>
<th>2^3</th>
<th>2^2</th>
<th>2^1</th>
<th>2^0</th>
</tr>
</thead>
<tbody>
<tr>
<td>32</td>
<td>16</td>
<td>8</td>
<td>4</td>
<td>2</td>
<td>1</td>
</tr>
</tbody>
</table>

"ones." ***first column***

| 1   | 0   | 0   | 0   | 0   |

Powers

Any number to the zero power is equal to one: \(2^0 = 1, \ 10^0 = 1\)

Any number to the first power is equal to itself:

\(2^1 = 2, \ 5^1 = 5, \ 10^1 = 10, \ 10^0 = 1\)

Refer to the Decimal/Binary chart. Notice the first column of binary: the highest you can count is one, then you start over again in that column with zero and have a "one" in the second or "two" column. For three we can add another unit to the "ones" column (a "1" in the twos column plus a "1" in the ones column equal three).

\(11_2 = 3_{10}\)
These rules apply to any of the binary columns.

\[ 100_2 = 4_{10} \]
\[ 101_2 = 5_{10} \]
\[ 110_2 = 6_{10} \] and so on

Try counting to ten in binary. Check your answers by referring to the Decimal/Binary chart.

<table>
<thead>
<tr>
<th>10</th>
<th>2</th>
<th>10</th>
<th>2</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td></td>
<td>6</td>
<td></td>
</tr>
<tr>
<td>1</td>
<td></td>
<td>7</td>
<td></td>
</tr>
<tr>
<td>2</td>
<td></td>
<td>8</td>
<td></td>
</tr>
<tr>
<td>3</td>
<td></td>
<td>9</td>
<td></td>
</tr>
<tr>
<td>4</td>
<td></td>
<td>10</td>
<td></td>
</tr>
<tr>
<td>5</td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>
Self-Test No. 1

Give decimal equivalent of these binary numbers.

1. 1011 = __________
2. 101 = __________
3. 100 = __________
4. 1000 = __________
5. 1111 = __________
6. 1010 = __________
7. 1001 = __________
8. 111 = __________
9. 1100 = __________

It should be noted that each output or input in a digital gate, circuit, or system may be likened to a single column of binary. Each column is known as a bit (binary digit).

The number of possible binary states from a given number of outputs or bits is 2 to that power. Say you have 2 outputs; you would have 2² or four states.

<table>
<thead>
<tr>
<th>outputs</th>
<th>1</th>
<th>2</th>
</tr>
</thead>
<tbody>
<tr>
<td>states</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td></td>
<td>0</td>
<td>1</td>
</tr>
<tr>
<td></td>
<td>1</td>
<td>0</td>
</tr>
<tr>
<td></td>
<td>1</td>
<td>1</td>
</tr>
</tbody>
</table>

4 outputs can give you 2⁴ or 16 states or conditions. 8 outputs can give you 2⁸ or 256 states or conditions. You can see that the number of states increases at a rapid pace once you get into higher numbers of binary output.

In both the decimal and the binary number system we use positional notation. There are three basic rules that apply to any number system that uses positional notation.

1. The number of digits or characters is equal to the base. (Decimal is base ten 0-9 and binary is base two 0-1.)
2. The largest digit is one less than the base (largest in decimal is 9 and largest in binary is 1).
3. Each digit is multiplied by the base raised to the appropriate power for the digit position. Higher digits increase by the next power of the base for each position to the left: the power of ten in decimal and power of 2 in binary.

For example, in the number "6 8 3 7" we see the "3" is multiplied by the base ten. The "8" is multiplied by the next higher power of the base (10^2 or one hundred). The "6" is multiplied by the next higher power of the base (10^3 and so on).

The same thing happens in base two. Each column to the left represents an increased power of two.

<table>
<thead>
<tr>
<th>TENS</th>
<th>TWOS</th>
</tr>
</thead>
<tbody>
<tr>
<td>10³ or 1000</td>
<td>2³ or 8</td>
</tr>
<tr>
<td>10² or 100</td>
<td>2² or 4</td>
</tr>
<tr>
<td>10¹ or 10</td>
<td>2¹ or 2</td>
</tr>
<tr>
<td>10⁰ or 1</td>
<td>2⁰ or 1</td>
</tr>
</tbody>
</table>

There are two other number systems that are used in digital electronics: octal, base 8 and hexadecimal, base 16.
Computer programmers or translators must work in the "machine language" of binary. It becomes very difficult to memorize the various binary sequences, much as you might memorize your zip code, driver's license, or telephone number. There is a higher degree of error when humans try to program in straight binary. But base 8, (0-7) and base 16, (0-F) compare to the decimal system and are compatible with each other through binary.

Any octal digit (0-7) can be represented by three digits of binary.

<table>
<thead>
<tr>
<th>OCTAL</th>
<th>BINARY</th>
<th>DECIMAL</th>
</tr>
</thead>
<tbody>
<tr>
<td>(Base 8)</td>
<td>(Base 2)</td>
<td>(Base 10)</td>
</tr>
<tr>
<td>0</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>1</td>
<td>1</td>
<td>1</td>
</tr>
<tr>
<td>2</td>
<td>10</td>
<td>2</td>
</tr>
<tr>
<td>3</td>
<td>11</td>
<td>3</td>
</tr>
<tr>
<td>4</td>
<td>100</td>
<td>4</td>
</tr>
<tr>
<td>5</td>
<td>101</td>
<td>5</td>
</tr>
<tr>
<td>6</td>
<td>110</td>
<td>6</td>
</tr>
<tr>
<td>7</td>
<td>111</td>
<td>7</td>
</tr>
<tr>
<td>10</td>
<td>1000</td>
<td>8</td>
</tr>
<tr>
<td>11</td>
<td>1001</td>
<td>9</td>
</tr>
</tbody>
</table>

AND IT COMES OUT EVEN!!

To convert from binary to octal, you start at the ones column and count off by threes toward the left.

```
  1 1 0 1 0 1 1 1 1 1
```

BINARY

Convert each three binary columns to an octal digit:

```
  001 101 011 111
```

OCTAL

1 5 3 7

To convert back from octal to binary you just convert each octal digit to three columns of binary (fill in zeros to the left, if there aren't three digits in the binary notation).

3\text{\_8} = 011\text{\_2}
Each digit of Hex (0-F) can be represented by four binary digits

<table>
<thead>
<tr>
<th>HEXADECIMAL (Base 16)</th>
<th>BINARY (Base 2)</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>0000</td>
</tr>
<tr>
<td>1</td>
<td>0001</td>
</tr>
<tr>
<td>2</td>
<td>0010</td>
</tr>
<tr>
<td>3</td>
<td>0011</td>
</tr>
<tr>
<td>4</td>
<td>0100</td>
</tr>
<tr>
<td>5</td>
<td>0101</td>
</tr>
<tr>
<td>6</td>
<td>0110</td>
</tr>
<tr>
<td>7</td>
<td>0111</td>
</tr>
<tr>
<td>8</td>
<td>1000</td>
</tr>
<tr>
<td>9</td>
<td>1001</td>
</tr>
<tr>
<td>A</td>
<td>1010</td>
</tr>
<tr>
<td>B</td>
<td>1011</td>
</tr>
<tr>
<td>C</td>
<td>1100</td>
</tr>
<tr>
<td>D</td>
<td>1101</td>
</tr>
<tr>
<td>E</td>
<td>1110</td>
</tr>
<tr>
<td>F</td>
<td>1111</td>
</tr>
</tbody>
</table>

Run out of decimal characters, use letters A-F.

To remember: 3-Fs = 1111
FINAL-F-FIFTEEN = 1111

Once within any of these three systems, conversions are extremely easy once you memorize the chart above. Any number in hex can be converted to binary then to octal. The reverse is also true.

CONSIDER: 5 7 3 6

- Convert each digit to binary: 101 111 011 110
- Regroup into groups of four: 1011 1101 1100
- Convert from binary to decimal: 13 14
- Convert to hex: B D E

Octal
Binary (grouped in threes)
Binary (grouped in fours)
Decimal equivalents of hexadecimal
Hexadecimal

CONSIDER: 11 13 14

- Convert each digit to binary: 1010 1101 1110
- Regroup into groups of four: 1011 1101 1100
- Convert from binary to decimal: 21
- Convert to hex: B D E

Octal
Binary (grouped in threes)
Same binary numbers (grouped in fours)
Decimal equivalents of hexadecimal
Hexadecimal
ADDITIONAL EXAMPLES

Binary to octal:
\[ 1110111011_2 = \ldots 8 \]
\[ 1110111011_2 = 3547_8 \]

Octal to binary:
\[ 637_8 = \ldots 2 \]
\[ 637_8 = 11011111010_2 \]

Binary to hex:
\[ 11101110110_2 = \ldots 16 \]
\[ 11101110110_2 = 39E6_{16} \]

Hex to binary:
\[ 6A7B_{16} = \ldots 2 \]
\[ 6A7B_{16} = 11010101110111_2 \]
Self-Test No. 2

Convert:

* from binary to octal
  Remember that you are working in binary grouped in threes.

1. 1011100 ________
2. 10101010 ________
3. 1111111 ________
4. 1111111 ________
5. 10000 ________
6. 110101 ________
7. 110001 ________
8. 1010110 ________
9. 1101101 ________
10. 1011101 ________

* from octal to binary

11. 537 ________
12. 623 ________
13. 425 ________
14. 142 ________
15. 100 ________
16. 72 ________
17. 10 ________
18. 111 ________
19. 666 ________
20. 777 ________
Self-Test No. 3

Try converting some hexadecimal and binary:

From binary to hex -

Remember that you are working in binary grouped in fours.

1. 101100
2. 10101010
3. 11111111
4. 11111111
5. 10000
6. 110101
7. 110001
8. 1010110
9. 1101101
10. 1011101

From hex to binary -

11. 537
12. 623
13. 425
14. F39
15. D15
16. BAD
17. 709
18. 619
19. 999
20. FFF
To convert from octal to hex or back, you first should convert to binary, then, starting from the right, group your binary columns in threes for octal and fours for hex. Then convert these binary groups to octal or hex digits and you will be finished.

**EXAMPLE I:**

Convert $25_8$ to hex

1. **Step 1.** Convert to binary $010101$
2. **Step 2.** Group in 4's $0101$
3. **Step 3.** Convert to hex $15$

Therefore $25_8 = 15_{16}$

**EXAMPLE II:**

Convert $25_{16}$ to octal

1. **Step 1.** Convert to binary $0010101$
2. **Step 2.** Group in 3's $001001$
3. **Step 3.** Convert to octal $45$

Therefore $25_{16} = 45_8$
Self-Test No. 4

Convert the following:

TO HEX

1. $37_{8} = \underline{\hspace{2cm}}$
2. $15_{8} = \underline{\hspace{2cm}}$
3. $777_{8} = \underline{\hspace{2cm}}$
4. $135_{8} = \underline{\hspace{2cm}}$
5. $472_{8} = \underline{\hspace{2cm}}$

TO OCTAL

6. $7B = \underline{\hspace{2cm}}$
7. $490 = \underline{\hspace{2cm}}$
8. $302 = \underline{\hspace{2cm}}$
9. $AAF = \underline{\hspace{2cm}}$
10. $43 = \underline{\hspace{2cm}}$
Self-Test No. 5

MATCH TERMS TO BLANKS

1. Digital electronic devices operate in the _______ number system.
2. Two number systems that are "compatible" with binary are _______ and _______.
3. The octal number system has _______ characters or symbols.
4. The hexadecimal number system has _______ characters or symbols.
5. The highest you can count in any one column of hexadecimal is _______.
6. The highest you can count in a single column of binary is _______.
7. The highest you can count in a single column of octal is _______.
8. Any octal digit can be represented by _______ columns of binary.
9. Any hexadecimal numeral can be represented by _______ columns of binary.

a. seven
b. four
  c. one
d. three
e. eight
f. hexadecimal
g. sixteen
h. binary
i. octal
j. fifteen or F
Self-Test No. 6

CONVERT:

1. $1010110_2$ to $_____8$.
2. $A539_{16}$ to $_____8$.
3. $4663_8$ to $_____16$.
4. $59D_{16}$ to $_____8$.
5. $110110110101_2$ to $_____16$.
6. $43_8$ to $_____2$.
7. $43_{16}$ to $_____2$.
8. $101_16$ to $_____8$.
9. $101_8$ to $_____16$.
10. $101_2$ to $_____16$. 
Final Test

CONVERT:

1. \( AAB_{16} \) to \( \quad \) 8.

2. \( 437_{16} \) to \( \quad \) 8.

3. \( 101110111_2 \) to \( \quad \) 8.

4. \( 427_8 \) to \( \quad \) 16.

5. \( 59F_{16} \) to \( \quad \) 8.

6. \( 377_8 \) to \( \quad \) 16.

7. \( 97_{16} \) to \( \quad \) 8.

8. \( 14_8 \) to \( \quad \) 16.

9. \( 3333_{16} \) to \( \quad \) 8.

10. \( 345_8 \) to \( \quad \) 16.
Final Evaluation

90% Accuracy on Final Test.

If all checks indicate OK, go on to next package.
Answers

Self-Test 1
1. 11
2. 5
3. 2
4. 4
5. 8
6. 15
7. 10
8. 9
9. 7
10. 12

Self-Test 2
1. 134
2. 252
3. 377
4. 177
5. 20
6. 65
7. 61
8. 126
9. 155
10. 135
11. 10101111
12. 110010011
13. 100010101
14. 1100010
15. 1000000
16. 10000101
17. 1000
18. 1001001
19. 11011011
20. 11111111

Self-Test 3
1. 2C
2. AA
3. FF
4. 7F
5. 10
6. 35
7. 31
8. 56
9. 6D
10. 5D
Self-Test 3 (Continued)

10. 10100110111
12. 11000100011
13. 10000100101
14. 111100111001
16. 110100010101
17. 111000011001
18. 1100011001
19. 100110011001
20. 111111111111

Self-Test 4

1. 1F
2. D
3. 1FF
4. 5D
5. 13A
6. 173
7. 2220
8. 1402
9. 5257
10. 103

Self-Test 5

1. Binary (h)
2. Octal (i), Hexadecimal (f)
3. 8 (e)
4. 16 (g)
5. For 15 (j)
6. 1 (c)
7. 7 (a)
8. 3 (d)
9. 4 (b)

Self-Test 6

1. 126
2. 122471
3. 9B3
4. 2635
5. 36ED
6. 100011
7. 1000011
8. 401
9. 41
10. 5
Goal:
The apprentice will be able to describe digital logic and gating systems.

Performance Indicators:
1. Describe analog.
2. Describe digital.
3. Describe digital logic families.
4. Describe logic gates.
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Objectives

Given:

- A set of final test questions.
- Equipment to perform a task of setting up digital and binary outputs of a binary counter.

The student will:

- Answer with an accuracy of 80%.
- Construct circuit properly to show digital and binary readouts.

Directions

Obtain the following:

- One 74193 binary counter IC
- One 7404 hex inverter IC
- One 7438 decoder decimal driver IC
- One FND 500 seven-segment LED display
- Four light-emitting diodes (LEDs)
- One 1 k 1/4 W resistor
- One 5 volt 1/2 amp regulated supply
- One push-button switch (double-pole, momentary-contact)

Learning Activities

1. Study Key Words list.
2. Read and study Information Sheets.
4. Do Final Test.
5. Do Task.
Key Words

Analog information: Information that is continuous in value and not limited to a fixed number of possible values.

Digital information: Information that is discontinuous in value and has a set number of possible values.

Binary: Having only two values or levels.

Logic family: A certain type of electronic circuitry that performs various digital logic operations. Different families perform the same operations using different circuitry.

Truth table: A table of binary values for the inputs and outputs of logic circuits describing the necessary input conditions to determine output conditions.

Flip-flop: A circuit having two outputs with opposite binary values. They can be used to store binary values for later use.

Gate: One of several types of digital logic circuits used to perform various digital logic operations such as AND, OR, NAND, NOR or NOT gates. (See Information Sheets for descriptions of specific gates.)
Information Sheet

Digital logic refers to the numerical operations performed by digital electronic circuits. The electronic circuits you have studied so far have been analog circuits. Digital circuits will be explained later on, but first a comparison between digital and analog methods of representing information will be given. This comparison should help to introduce the subject of digital electronics.

Analog information is continuous. The number of possible values is unlimited. Digital information is not continuous. The number of values is limited or fixed depending upon the complexity of the digital information. This is similar to the difference between going up steps or a ramp. The steps have a fixed number of intermediate levels between two levels whereas the ramp has a continuous number of levels. You may stop on the ramp in a place that would be in between two steps on the series of steps.

Examples of devices that display information in analog form are an ordinary clock or watch, a mercury thermometer, or a meter (VOM) scale.

There is no limit to the number of positions at which the hands of the clock, the position of the mercury in the thermometer, or the pointer on the VOM scale can rest. The time information is represented continuously from 0 to 60 minutes for the minute hand. The temperature information is continuous from -40 degrees to +120 degrees Fahrenheit for the thermometer, and the voltage information is continuous from 0 to 10 volts for the voltmeter. In theory, any possible values between these limits could be read from these analog devices remembering that the values read could have fractional or decimal parts (i.e., 10½ minutes or 5.75 volts, etc.).

In contrast to this, each of the above instruments has a digital counterpart. The digital instruments have a fixed number of digits in their readouts. For instance, there is a fixed number of minutes that can be read from a digital clock (exactly 60), and also a fixed number of degrees, 160 in this example, are read from the digital thermometer. Only ten values of voltage can be read from the digital voltmeter in the example given.
Many times digital instruments will also give decimal parts such as 5.3 volts instead of just 5 volts. This will increase the number of possible values, but the number is still limited. On the other hand, with analog instruments the number of values that can be read is unlimited, at least in theory. In practice, however, I'm sure you will agree that even the analog instruments are limited by reading accuracy or the ability to determine the difference between two very close positions of the indicator on the scale. The reading accuracy, therefore, is the only thing that limits the number of possible values for analog information. In terms of electrical signals, an analog signal such as a voltage is one that is continuous in voltage. It may have any possible voltage value within the range of values over which it exists. A digital signal has a set or limited number of values or levels within its range.

The most common digital signal is a binary signal which has only two possible values. These two values are usually called a 0 (zero) and a 1 (one). In practice the values might be 0 volts for the zero and 5 volts for the one or any other set of two different values.

How can information having many possible values be represented by a binary electrical signal if the binary signal has only two values? The answer is by using a sequence of several binary values (0s and 1s) to represent a single larger value. You learned how to convert decimal numbers to their binary equivalent values in the last package. For instance, the sequence of binary values 1101 would represent the decimal number 13. In general the larger the number of values of a quantity to be represented by a binary sequence, the longer the sequence must be.

There have been several levels of development in the history of digital electronics. Early computers used relays with open contacts, then vacuum tubes, then discrete transistors. Most recently came integrated circuits (ICs).
Digital electronic circuits perform basically the same functions that are performed by a series of mechanical switches. When a switch is closed, the binary value of 1 is indicated and when it is open the value of 0 is indicated.

Rather than actually using mechanical switches, various types of electronic circuits perform the equivalent switching functions. The digital circuits that do this are classified into different categories called logic families. The logic families differ in that they use different types of circuits to perform the same functions. The following list shows some of the commonly used digital logic families. The functions that these logic families all perform can be categorized into the following major functions:

- AND gates
- NAND gates
- OR gates
- NOR gates
- NOT gates (INVERTERS)
- FLIP-FLOPs

Some other logic families include the following:

- RTL (Resistor-transistor-logic)
- DTL (Diode-transistor-logic)
- TTL (Transistor-transistor-logic)
- PMOS & NMOS (Positive and negative MOS)*
- CMOS (Complimentary MOS)*

*Metal oxide semiconductor

This is only a partial listing and each has its own characteristic size, power consumption and operating speed. Each group is called a family and is not usually interchangeable with the others because of voltage requirements, speed, polarity and pin layouts.

In the following explanations of the functions that these digital circuits perform, a mechanical switch equivalent is also given to help demonstrate the function of the circuit. Only the symbol for the function performed will be given since the circuitry will vary depending on the logic family that is used.

A "1" represents one of the binary values (usually the larger voltage) and also is represented by a closed switch in the switch example. A "0" represents the other binary value (usually the smaller voltage) and an open switch. The inputs are shown on the left side of the symbol and the output on the right. The tables at the right, called truth tables, indicate all possible operating conditions for the inputs and outputs. On any given row in the tables, for the input conditions indicated, the output condition will be as shown.
**The AND Gate**

**Symbol**

\[ \text{Symbol} \]

**Switch Example**

\[ \text{Switch Example} \]

**Truth Table**

\[
\begin{array}{ccc}
A & B & C \\
0 & 0 & 0 \\
0 & 1 & 0 \\
1 & 0 & 0 \\
1 & 1 & 1 \\
\end{array}
\]

In the AND gate both the inputs must be high to get a high output. A AND B must be 1 to get a 1 at C.

AND gates may have more than two inputs, but all of the inputs must be high for the output to be high. Look at the lamp in the example of our AND gate. Consider the lamp asking if all the switches are closed. It will not respond by turning on until all the switches are closed.

**The OR Gate**

**Symbol**

\[ \text{Symbol} \]

**Switch Example**

\[ \text{Switch Example} \]

**TRUTH TABLE**

\[
\begin{array}{ccc}
A & B & C \\
0 & 0 & 0 \\
0 & 1 & 1 \\
1 & 0 & 1 \\
1 & 1 & 1 \\
\end{array}
\]

The OR gate will have a high at the output if either OR both inputs are high. OR gates may also have more than two inputs.

**The NOT Gate (inverter)**

**Symbol**

\[ \text{Symbol} \]

**Example**

\[ \text{Example} \]

**TRUTH TABLE**

\[
\begin{array}{c|c}
F & \bar{F} \\
0 & 1 \\
1 & 0 \\
\end{array}
\]

The inverter or NOT gate merely changes the input to its opposite state. So if we get 1 in, we get a 0 out (the little bar over the top designates the opposite: \( \bar{F} \) would be spoken as "not F").
Take a look at the example of the inverter. If is low which turns off the transistor switch. Since this is equal to an "open" to ground, our will remain at the +5 volt source level. When it goes high, it will forward bias the emitter and turn the transistor on. This will tie to ground or low. So when is high, is low and when is low, is high.

The laid-over triangle is the schematic symbol for an amplifier or buffer. The little circle is the actual symbol for inverter.

These three gates are the basis for all logic circuits. Everything else is generally constructed from these three gates. Sounds too simple, doesn't it? But think. These gates are the equivalents of simple on/off switches. Let's look at some "combination gates" and their truth tables.

**NAND gate (Negative AND)**

<table>
<thead>
<tr>
<th>A</th>
<th>B</th>
<th>C</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>0</td>
<td>1</td>
</tr>
<tr>
<td>1</td>
<td>0</td>
<td>1</td>
</tr>
<tr>
<td>0</td>
<td>1</td>
<td>1</td>
</tr>
<tr>
<td>1</td>
<td>1</td>
<td>0</td>
</tr>
</tbody>
</table>

**COMPARE**

<table>
<thead>
<tr>
<th>A</th>
<th>B</th>
<th>C</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>1</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>1</td>
<td>1</td>
<td>1</td>
</tr>
</tbody>
</table>

**NOR gate (Negative OR)**

<table>
<thead>
<tr>
<th>A</th>
<th>B</th>
<th>C</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>0</td>
<td>1</td>
</tr>
<tr>
<td>0</td>
<td>1</td>
<td>1</td>
</tr>
<tr>
<td>1</td>
<td>0</td>
<td>1</td>
</tr>
<tr>
<td>1</td>
<td>1</td>
<td>0</td>
</tr>
</tbody>
</table>

The outputs of each are the opposite of AND and OR gates. This is indicated by the little circle on the right of the standard AND and OR symbols. These gates can be purchased in an "individual-package" form in I.C. chips that have 1 or more gates in them. Some LSI (large-scale integrated) circuits have hundreds of gates. These I.C.s can have from 4 to 40 pins.

**FLIP-FLOPs**

Two output states

State 1\( Q = 1, \bar{Q} = 0 \)

State 2\( Q = 0, \bar{Q} = 1 \)
A flip-flop stores a binary value until an input changes its value. The flip-flop has two outputs and can have one or more inputs. Each output is either a 1 value or a 0 value and each is the opposite value of the other. In other words, if one of the outputs is a 1, the other will be a 0 and vice-versa. The input signal or signals have the capability of changing the condition of the outputs. Notice that there are just two conditions or states for the outputs. Flip-flops are used for many different operations in digital systems.
Self-Test

Match numbered terms to blanks in questions 1 through 5.

1. A thermometer or an electric clock can have either a ____________ or ____________ readout.

2. Types of circuits that are used in digital circuits are compatible within their ____________

3. Three basic gates are the ____________ and ____________

4. Digital electronic circuits perform basically the same functions that can be performed by mechanical ____________

5. A digital circuit having two binary outputs of opposite value is a ____________

   a. AND
   b. families
   c. NOT
   d. analog
   e. switches
   f. OR
   g. digital
   h. flip-flop

6. Your automobile has both a speedometer and an odometer. The speedometer lets you know how fast you are going, the odometer how far you have gone. One of these is a digital instrument and the other is analog. Which is which? ____________
Final Test

1. Truth tables show all possible input and output conditions for _____________________________.

2. A _______________________ gate is a combination of an AND and a NOT gate.

3. Different classes of circuitry used to perform the same types of digital logic functions are called logic _________________.

4. An automobile speedometer output is _________________.

5. The odometer output is _________________.

6. Digital circuits perform functions that can also be performed by _________________.

7. An inverter is the same as a _________________.

8. If all inputs of a gate must be binary 1 to get an output of binary 1, the gate is called a(n) _________________.

a. OR
b. AND
c. logic gates
d. NAND
e. NOR
f. families
g. NOT
h. mechanical switches
i. digital
j. analog
Task

EQUIPMENT

One 74193 binary counter IC
One 7404 hex inverter IC
One 9368 decoder decimal driver IC
One FND 500 seven-segment LED display
Four light-emitting diodes
One 1 k 1/4 W resistor
One 5 volt 1/2.amp regulated supply
One push-button switch (double-pole, momentary-contact).

PROCEDURE:

Hook up the circuit as shown. Be sure you don't bend pins on ICs. Watch polarity on LEDs (keyed side is cathode). Pins on the devices are laid out as shown below.
This circuit consists of a 74193 binary counter, a 9368 binary-to-seven-segment decoder-driver, and a seven-segment LED display. The four binary outputs of the 74193 counter drive the LED indicators L1 through L4 and the decoder driver. The output of the decoder-driver is used to drive the seven-segment display. The 7404 hex inverter is used to drive the LED binary indicators, as the 74193 IC will not have power capabilities to drive the LEDs. Two of the inverters are also used to insure proper switching performance. The 74193 will count in binary from zero to fifteen. This will be displayed by the four LEDs. The 9368 will convert the binary to the output needed for the seven-segment display. Transistors in the 9368 will provide required power to drive the seven-segment output.

You should be able to see the numbers change on the seven-segment display and, at the same time, the equivalent binary number displayed on the LEDs.

For instance, when the seven-segment display shows the number 9, the top LED (LSD) and the bottom LED (MSD) should be lit but the other two LEDs unlit representing the binary number 1001. LSD refers to least significant digit (representing a 0 or a 1) and MSD refers to most significant digit (representing a 0 or an 8).
Answers

1. g and d
2. b
3. a, c and f
4. e
5. h
6. Speedometer is analog; odometer is digital.
Goal:

The apprentice will be able to use common computer terminology.

Performance Indicators:

1. Use common terms.
2. Use common acronyms.
BASIC ELECTRONICS

Computer Overview
EL-BE-56

Test Draft
September 1981
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Objectives:

Given:
A group of computer acronyms.
A set of true/false statements concerning computers.

The student will:
Write out the terms the acronyms represent.
Correctly identify as true or false within an accuracy of 90%.

Directions
Obtain the following:
Paper and pencil

Learning Activities
- Study Key Words List.
- Read and study Information Sheets.
- Do Self-Test.
- Do Final Test.
- Obtain Final Evaluation.
Key Words

**Buzz Words:** The terms used as jargon in the computer field.

**Central Processing Unit (CPU):** The main processing part of the computer.

**Mini-Computer:** A smaller, stand-alone computer that requires no special environment.

**Micro-Computer:** A computer designed around a microprocessor chip.

**Main Frame:** A regular computer installation which generally requires a special air-conditioned environment.

**LSI:** Large-Scale Integrated circuit.

**Hardware:** Physical components and supplies used in computing.

**Software:** Instructional or program sequences and organization used in computer operations.

**Acronym:** A word formed by the first letters in the words of a phrase. ROM = Read-Only Memory.

**RAM:** Random-Access Memory. A memory section of a computer that can store information, either data or instruction, at the first available location, and if it runs out of space at that location, will proceed to the next available memory address or location.

**Scratch Pad:** The computer's temporary memory that is used as you might use a scratch pad to jot down notes to which you wish to refer.

**ALU:** Arithmetic Logic Unit. The portion of the ROM that performs mathematical computations.

**BYTE:** A memory word that consists of eight binary digits.

**BIT:** Binary Digit. The basic unit used in computing.

**LPS:** Lines Per Second. Used to determine printing speed.

**CPS:** Characters Per Second. Used to determine printing speed.

**CRT:** Cathode-Ray Tube. A TV screen.

**Machine Language:** The binary switching logic the computer deals with. The first level of computer language.

**Assembly Language:** The second level of computer language that translates the higher symbolic compiler languages to the machine language.

**Compiler Language:** The third level of language that is more like the language we speak. BASIC and COBOL are compiler languages.
Fortran: A computer language used in science.

Cobol: A computer language used in business.

Basic: A computer language designed for engineering use.

Subroutine: A section of a program that can stand alone and may be reused.

Data: That information the computer processes.

ROM: Read-Only Memory: The controlling section of the computer's memory.
The information contained in this learning package will help you understand some of the history and "state-of-the-art" generations of computers. Study the package carefully and learn the terms. When you hear them later, they will make sense to you. Many of us are separated from the computer world by the terms and "buzz words" of the field.

The computer of today has been developing since the early 1940s. The original computers were primarily for use in performing calculations of numbers. Today's equipment is used for processing any kind of information. This period is becoming known as the "Information Age," and computers are the reason. Memory storage and access speeds are increasing almost daily, and the costs of computers are coming down rapidly. This progress is touching all of our lives and will continue to do so. There will be many employment opportunities in the design, use, maintenance and repair of this equipment. It is likely that there will be very few occupations that won't need some background in this area.

The early computers of the 40s and 50s were very large and used vacuum tubes as the electronic switches. These machines used a great deal of power and gave off a great deal of heat. The central processing unit (CPU), or mainframe and memory storage equipment, was in a special air-conditioned room to keep the equipment clean and cool. Even today, many computer hardware installations require these kinds of special environments. In 1948 the transistor was developed. The transistor made computers much smaller and more reliable. Though temperature does affect all electronic equipment, the more modern information processing equipment consumes less power, so doesn't generate as much heat.

Many of today's computers are smaller in size, and new technology has grown from several directions, evolving minicomputers and microcomputers. Developments within these "size-oriented" divisions have occurred so rapidly that it's difficult to define them. Generally, though, the minicomputer is one that is smaller in size than the earlier main-frame computers and doesn't need a special environment. The microcomputers developed around the microprocessor, which is a single large-scale integrated-chip (LSI) that can actually process commands and be expanded by adding memory.

Computing or information processing is composed of two interdependent divisions: the hardware and the software. Hardware is the sum total of all the electronic components, memories, keyboards, CRTs, printers, tapes, discs and cards. All of the physical equipment is hardware. Software is the program or the instructions that direct the computer in its processing activities.

The computer's program language (machine language) is the binary number system. Humans, however, do not deal with binary so well. Higher level program languages have been developed for humans to communicate with computers, beginning with...
octal and hexadecimal. Assembly languages, which are symbols and codes, are used to communicate with the machines. These are still only numbers and symbols. To do all of our communicating with only numbers and code symbols is very awkward. Therefore, more complex, higher level languages were developed. The languages initially were developed by computer companies for their own machines, and then languages were developed for particular applications. FORTRAN (FORMula TRANslation) was developed for the scientific community. COBOL (COMmon Business Oriented Language) is used to express problems in data processing in English narrative form. BASIC is a simplified program language intended for use in engineering applications. There have been many other languages designed for certain hardware and user applications, but these three have been the most widely applied.

The development of software has been slower than the advancements in hardware technology, but predictions promise an explosion of software in the very near future. With the availability of personal computers and the increasing capabilities of hardware and standardization of phrases and subroutines, we should witness a very exciting era of progress.

The hardware portion of a computer consists of several major components. All computers have an input, an output, a central processing unit (CPU), and to a greater or lesser extent, a memory.

Inputs can be as simple as a voltage sensor, light sensor, or switch. The keys on a computer terminal keyboard are switches.

The output may be a solenoid-driven printer or a cathode-ray tube (CRT) or, perhaps, a single lamp or LED. It may switch the power to a servo motor (control motor) of a computerized manufacturing machine or trigger the data through the electromagnetic recording head of a computer tape or disc pack. Some may serially dump pulsating square wave signals to generate audio signal frequency to simulate a human voice.

The Central Processing Unit, or CPU, is where the input data is recognized and stored as instructed by the program or the controlling section of the computer—the Read-Only Memory (ROM). The ROM will process the data and will use the Random-Access Memory (RAM) for temporary storage for "scratch pad" use and proper sequencing. The Arithmetic Logic Unit (ALU), which is part of ROM, will process the mathematics.

Once the information has been processed properly, it may be stored on a type of storage media such as a magnetic tape or disc.

Computers of the seventies would commonly have 64 k of memory. 64 k stands for sixty-four kilobyte or sixty-four thousand bytes of internal RAM. A byte is an 8-bit word. Remember a bit is a Binary digit or a serial pulse on a two-state data track. It is not uncommon today for computers to have 96 megabytes or 96 million bytes of Random-Access Memory and be smaller than their ten-year-old...
counterparts. The size and density of these systems are increasing their power. The access and output speed is another ingredient in their power. Laser printers print at speeds of 900 Lines Per Second (LPS) and are still too slow. The maximum printing speed of an IBM Selectric Typewriter is fifteen and one-half Characters Per Second (CPS). Daisey wheel printers are driven at around seventeen to fifty CPS. Dot matrix printers and belt or chain printers may print at three to 40 LPS.

Computers are now an integral part of our lives and will continue to expand further into the 80s and the 90s.

In this time of rapid change in the economic and technical worlds, many careers will change dramatically or even die out. This is not likely to happen to you if you choose a career in the computer field.
Self-Test

Acronyms are words formed by the first letters of a group of words that is frequently used. Below you will find a list of acronyms. Write out the phrases that these acronyms stand for.

1. LSI
2. ALU
3. CRT
4. BIT
5. LPS
6. RAM
7. ROM

True/False

8. Computers of today have many vacuum tubes. ______________
9. Software technology is way ahead of hardware technology. ______________
10. 96 megabytes is a memory possible today. ______________
11. The input to a computer may be a switch or group of switches. ______________
12. ROM stores random memory. ______________
13. Magnetic tape is a type of storage media. ______________
14. COBOL is a compiler language. ______________
15. The transistor was invented in 1948. ______________
16. The binary number system is the easiest computer language for humans to use. ______________
17. A byte is 24 bits. ______________
Final Test

Write out proper phrase for the acronym.

1. ROM 
2. RAM 
3. CRT 
4. ALU 
5. LPS 
6. LSI 
7. BIT 

True/False

8. Storage media may be a magnetic disc. 
9. A byte is 64 bits. 
10. 900 LPS is fast printing. 
11. FORTRAN is a common output device. 
12. The transistor was developed in 1938. 
13. It is an easy task for humans to program in straight binary. 
14. Memory in computers can be very large or quite small. 
15. A career in computer service is extremely limited.
Final Evaluation

90% accuracy on Final Test
Answers

Self-Test Answers

1. Large-Scale Integrated
2. Arithmetic Logic Unit
3. Cathode-Ray Tube
4. Binary Digit
5. Lines Per Second
6. Random-Access Memory
7. Read-Only Memory
8. F
9. F
10. T
11. T
12. F
13. T
14. T
15. T
16. F
17. F
Goal:
The apprentice will be able to describe proper care and handling of software.

Performance Indicators:
1. Describe types of software.
2. Describe handling of software.
3. Describe identification of software.
• Study Guide

• Read the goal and performance indicators to find what is to be learned from package.
• Read the vocabulary list to find new words that will be used in package.
• Read the introduction and information sheets.
• Complete the job sheet.
• Complete self-assessment.
• Complete post-assessment.
Vocabulary

* Cassette
* Central processing unit (CPU)
* Diskette
* Floppy disk
* Hard disk
* Magnetic tape
* Mainframe
* Microcomputer
**Introduction**

Modern steam plants use the computer to control many of the plant functions that were previously operated manually. The process steps are programmed into a computer so that each command is given in the proper sequence.

The computerized controls may be part of a central processing unit (CPU) which uses a computer mainframe to operate the program. A mainframe is a computer that requires a controlled environment and has vast memory storage capacity. In other cases it may be operated by a microcomputer that uses diskettes or cassettes as software. A microcomputer is a computer designed around a microprocessor chip.

This package is merely an introduction to software. The operator must complete further study of software packages that are specific to the type of computer being used with the software.
Types of Software

Microcomputers use two types of software. The magnetic disk or diskette commonly used form of software. This diskette may be of the floppy disk type or a hard disk type. In either case, the data is stored on a magnetic disk. Another form of software is magnetic tape or cassette. In the cassette, the computer program is recorded on the cassette and the cassette, in turn, gives commands to the computer.

Handling Software

Software should be maintained in a dust-free file. Cassettes and diskettes are small and can be stored in a plastic container that is smaller than a shoebox. The operator should be careful not to touch the grooved portion (center) of the diskette. This can damage the data stored on the diskette. Common sense will provide direction in software handling. Diskettes must not be bent or have materials piled on them that might damage the disk.

Identification of Software

Software packages should be clearly marked and filed in alphabetical order. This allows the operator to find the proper software quickly. Small labels should be attached to the cardboard diskette holder. The name of the program should be clearly marked on the labels with a black pen. For software that controls the processes of steam generation, the diskettes or cassettes should be labeled by function such as "Start up Boiler" or "Control Steam Temperatures".
Assignment

* Complete the job sheet.
* Complete the self-assessment and check answers.
* Complete the post-assessment and have the instructor check your answers.
VISIT A COMPUTER STORE

* Visit a local store that sells microcomputers, i.e.' Radio Shack, Commodore, IBM.

* Ask to see their software catalog.

* Identify software that is appropriate for use in a power plant.

* Ask salesperson to demonstrate diskette and/or cassette use, care and storage.
Self Assessment

Match the following words and phrases.

1. Main frame

2. Diskette

3. Cassette

4. Microcomputer

5. Diskette holder

A. Type of magnetic tape

B. Labels should be attached to this part.

C. Designed about a microprocessor chip.

D. Central Processing Unit Computer

E. Type of magnetic disc
Self Assessment Answers

1. D
2. E
3. A
4. C
5. B
Post Assessment

1. A ______________ computer requires a controlled environment and has large memory storage.

2. A ______________ is built about a microprocessor chip.

3. List two types of disks.

4. What is the common name of magnetic tape software?

5. What is the common name of magnetic disk software?
Instructor

• Post Assessment Answers

1. Mainframe
2. Microcomputer
3. Floppy, Hard
4. Cassette
5. Diskette
Supplementary References

Sales literature from computer companies will provide understanding of software terminology and specifications.