

DOCUMENT RESUME

ED 057 578

EM 009 418

AUTHOR Hurtado, Marco; Rosenbaum, Fred J.  
TITLE A CW Gunn Diode Switching Element.  
INSTITUTION Washington Univ., St. Louis, Mo.  
SPONS AGENCY National Aeronautics and Space Administration,  
Washington, D.C.  
REPORT NO M-71-12  
PUB DATE Nov 71  
NOTE 11p.  
EDRS PRICE MF-\$0.65 HC-\$3.29  
DESCRIPTORS \*Communication Satellites; Electric Circuits;  
\*Electromechanical Technology; Electronics

ABSTRACT

As part of a study of the application of communication satellites to educational development, certain technical aspects of such a system were examined. A current controlled bistable switching element using a CW Gunn diode is reported on here. With modest circuits switching rates of the order of 10 MHz have been obtained. Switching is initiated by current pulses of short duration (5-10 nanoseconds). Rise times of the order of several nanoseconds could be obtained. The apparent mechanism which allows the bistable operation of the Gunn diode is the reduction in the mobility of the GaAs active layer due to device heating. (Author/JY)

ED057578



# WASHINGTON UNIVERSITY

---

Memorandum No. 71-12

November, 1971

U.S. DEPARTMENT OF HEALTH,  
EDUCATION & WELFARE  
OFFICE OF EDUCATION  
THIS DOCUMENT HAS BEEN REPRO-  
DUCED EXACTLY AS RECEIVED FROM  
THE PERSON OR ORGANIZATION ORIG-  
INATING IT. POINTS OF VIEW OR OPIN-  
IONS STATED DO NOT NECESSARILY  
REPRESENT OFFICIAL OFFICE OF EDU-  
CATION POSITION OR POLICY

## A CW Gunn Diode Switching Element

**Marco Hurtado**

**Fred J. Rosenbaum**

ED057578

A CW GUNN DIODE BISTABLE SWITCHING ELEMENT

M. Hurtado and F. J. Rosenbaum

Abstract

A current controlled bistable switching element using a CW Gunn diode is reported. With modest circuits switching rates of the order of 10 MHz have been obtained. Switching is initiated by current pulses of short duration (5 - 10 ns). Rise times of the order of several nanoseconds could be obtained. The apparent mechanism which allows the bistable operation of the Gunn diode is the reduction in the mobility of the GaAs active layer due to device heating.

## A CW Gunn Diode Bistable Switching Element

M. Hurtado<sup>+</sup> and F. J. Rosenbaum<sup>+</sup>

The use of long, heavily doped samples of GaAs as pulse generators<sup>1-3</sup> and pulsed logic elements has been reported by many workers.<sup>4-11</sup> The purpose of this letter is to describe the use of CW Gunn diodes as bistable switching elements.

The well known static characteristic of GaAs, its average velocity versus electric field, exhibits a region of negative slope.<sup>12, 13</sup> Consequently, the terminal dc current-voltage characteristic of a CW GaAs diode can also exhibit a corresponding negative differential resistance region which will depend on the device characteristics, the bias circuit configuration, and the microwave circuit loading on the device. For example see Ref. 14. A suitable load line determined either by the series or parallel connection of the device with an external load resistor can be made to intersect the static i-v curve in three points as shown in Fig. 1. Point a is below the threshold voltage and the device is quiescent there. Microwave power is generated when the device is biased at point (b).

The i-v curves of numerous devices were measured with the diodes mounted in a tunable X-band waveguide holder. The

---

<sup>+</sup> Electrical Engineering Department, Washington University, St. Louis, Missouri 63130. This work was supported by NASA under Grant No. Y/NGL-26-008-054.

device active layer lengths were nominally  $10\ \mu\text{m}$  and the doping densities were near  $1 \times 10^{15}\ \text{cm}^{-3}$ .

Figure 2 shows a typical terminal i-v characteristic as displayed on a curve tracer swept at 60 Hz. Negative differential resistance behavior is clearly seen. The shape of the curve could be drastically altered by the microwave tuning, in some instances showing multiple regions of negative slope. For some tuning conditions several hundred milliwatts of X-band power could be generated.

The most striking feature of Fig. 2 is the presence of a hysteresis-like loop. The upper trace occurs during the increasing part of the voltage cycle; the lower trace occurs after the maximum voltage has been applied and the voltage is decreasing. This loop is not due to a phase difference or delay in the measurement circuits. Notice that the maximum current on the lower trace is less than that on the upper trace.

Our explanation for the hysteresis loop is the difference in the operating temperature of the device at each point along the i-v characteristic. During the increasing portion of the applied voltage cycle the operating temperature of the device is rising. The maximum dissipation occurs at the maximum applied voltage, since the decrease in current from threshold to this point is rather small, (about 20%), while the applied voltage has doubled. On the downswing the device begins to cool. Notice that at any voltage the device is hotter on the downswing than on the upper trace.

To corroborate this presumed temperature effect the i-v characteristics of several devices were observed when the heat sink temperature was varied from 27.3° to 83°C. The hysteresis loop was maintained but the current was reduced at all voltages as the heat sink temperature was increased.

Devices exhibiting the largest current drop-back between threshold and maximum voltage were selected and tested in the experimental switching circuit shown in Fig. 3. The load resistor  $R_L$  is chosen from the curve tracer results to yield a load line which intersects the i-v curve below threshold and at a high voltage ( $10 < V < 15$  volts). The device is current-biased slightly below threshold. When a positive current pulse is added the device switches to its high voltage, lower current stable point. The amplitude of this current pulse must be such that the total current delivered to the circuit exceeds the device threshold current. A current pulse of opposite polarity which exceeds the minimum device current switches the device back to its original high current, low voltage operating point.

Figure 4 shows the voltage across  $R_L$  as a function of time as the device is being switched between its two states at 500 ns intervals. The lower voltage is 5 volts and the upper voltage is 8 volts. Also noticeable in the figure are the positive and negative switching pulses, whose pulse width is about 100 ns. The maximum switching rate which was obtained was 10 MHz. Switching could be initiated with pulses as short as 5-10 ns. Rise times of the order of several nanoseconds have been observed.

One factor that limits the rise time is the bias circuit oscillations which are often excited during the switching period<sup>2</sup>. This may be due to the mechanical arrangement of our waveguide, bias, and pulse circuits. It is expected that the use of a microstrip circuit would reduce the many parasitic reactances and improve the performance of the device.

An interesting logic feature of this bistable switch is the microwave signal that is generated in the high voltage state. It could be detected to provide a non-destructive read out of the state of the switch, or used for other logic operations which might require a bit stream of microwave pulses.

The switch could be set indefinitely in either of its two states in contradiction to the prediction of Englemann and Heinle.<sup>15</sup> In a recent paper Thim<sup>16</sup> suggested that a stationary field configuration could be initiated in overcritically doped samples ( $nL \geq 10^{12}$ ) which would result in a bistable switching element. That device would not produce any microwave oscillations however. The observations reported here lead us to conclude that it is the reduction of the mobility of the GaAs active layer, caused by the dc power dissipation, which gives rise the bistable characteristic with microwave power generation in the high voltage state.

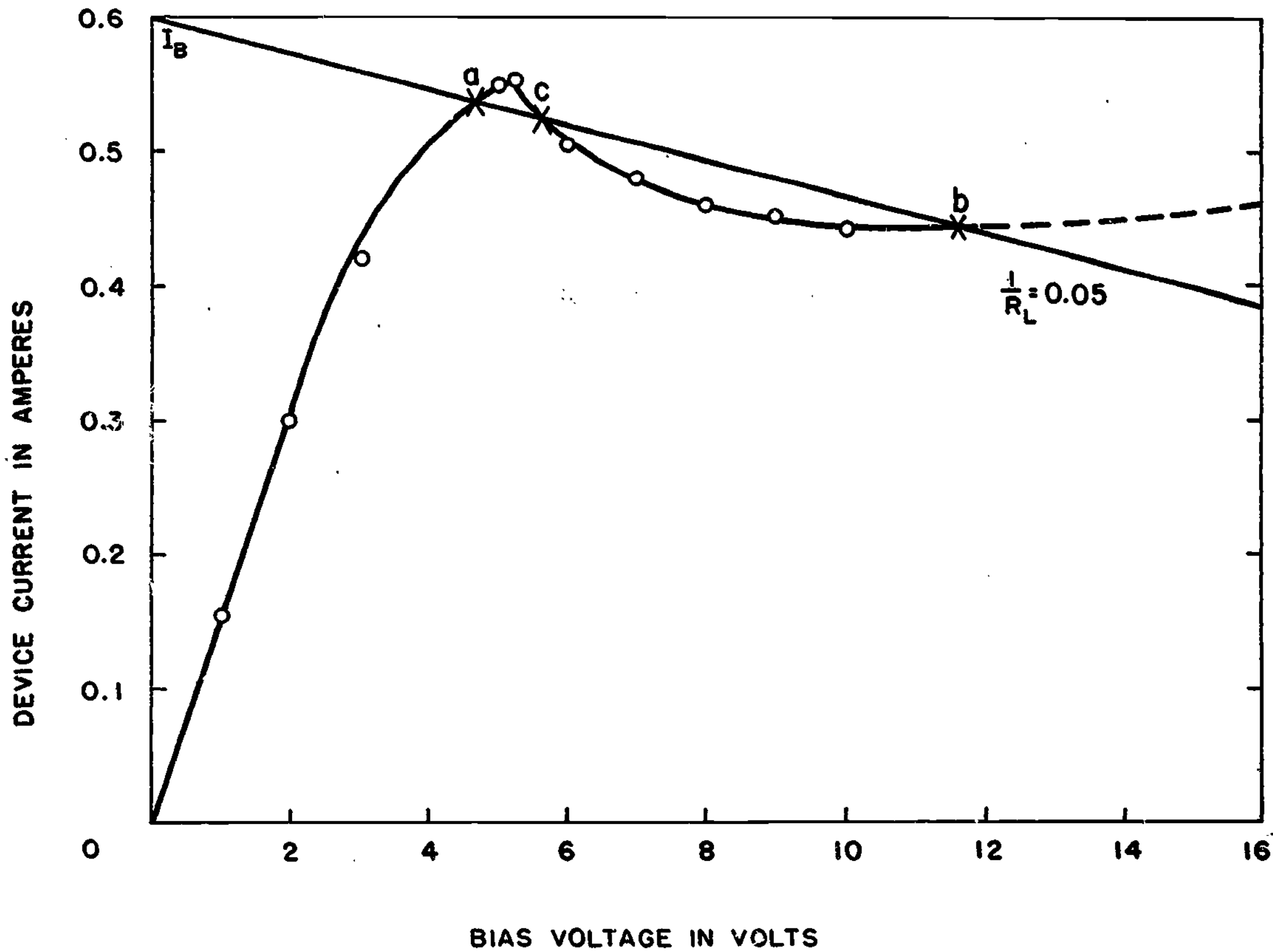
### References

1. R.E. Fisher, "Generation of Subnanosecond Pulses with Bulk GaAs," Proc. IEEE (Letters) vol. 55, pp 2189-2190, December 1967.
2. W. C. Tsai and F. J. Rosenbaum, "Bias Circuit Oscillations in Gunn Devices," IEEE Trans. on Electron Devices, vol. ED-16, pp. 196-202, February 1969.
3. C. Lanza and R.M. Exposito, "Bulk Negative Resistance Device Operated in a Relaxation Mode," Solid State Electronics, vol. 12, pp. 463-467, 1969.
4. J. A. Copeland, T. Hayashi, and M. Uenohara, "Logic and Memory Elements Using Two-Valley Semiconductors," Proc IEEE (Letters), vol. 55, pp. 584-585, April 1967.
5. H. L. Hartnagel, "Digital Logic-Circuit Applications of Gunn Diodes," Proc. IEEE (Letters), vol. 55, pp. 1236-1237, July 1967.
6. S. H. Izadpanah and H. L. Hartnagel, "Experimental Verification of Gunn-Effect Comparator," Proc. IEEE (Letters), vol. 55, pp. 1748, October 1967.
7. H. Hartnagel, "Some Basic Logic Circuits Employing Gunn-Effect Devices," Solid-State Electronics, vol. 11, pp. 568-572, 1968.
8. H. L. Hartnagel, "Theory of Gunn Effect Logic," Solid-State Electronics, vol. 12, pp. 19-30, 1969.
9. T. Sugeta, H. Yanai, and T. Ikoma, "Switching Properties of Bulk-Effect Digital Devices," IEEE Trans. on Electron Devices, vol. ED-17, pp. 940-942, October 1970.
10. S. Kataoka, K. Komamiya, and M. Morisue, "A High-Speed Adder Using Gunn Diodes," Proc. IEEE (Letters), vol. 59, pp. 1526-1527, October 1971.
11. S. Kataoka, H. Tateno, M. Kawashima and M. Morisue, "A Proposal for a Full Adder Using Gunn Diodes," op cit, pp 1527-1528.
12. D. E. McCumber and A. G. Chynoweth, "Theory of Negative-Conductance Amplification and of Gunn Instabilities in Two Valley Semiconductors," IEEE Trans. on Electron Devices, vol. 10, pp. 40-42, January 1967.

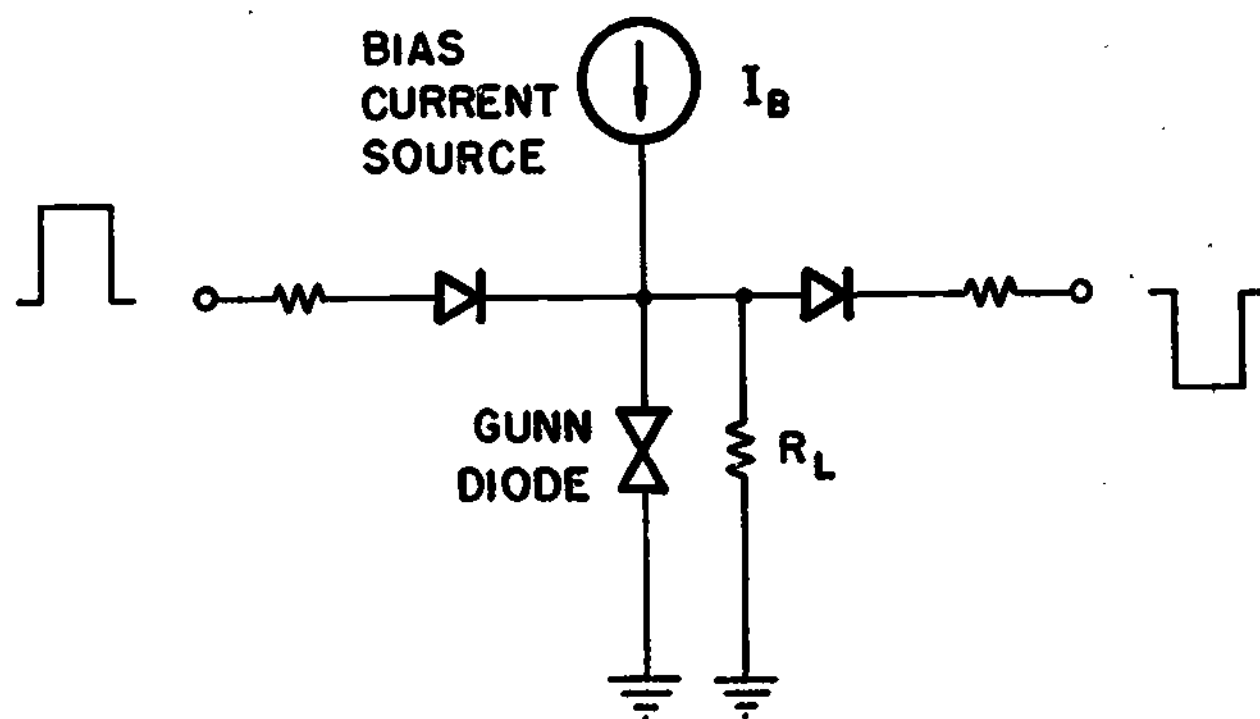


References (continued)

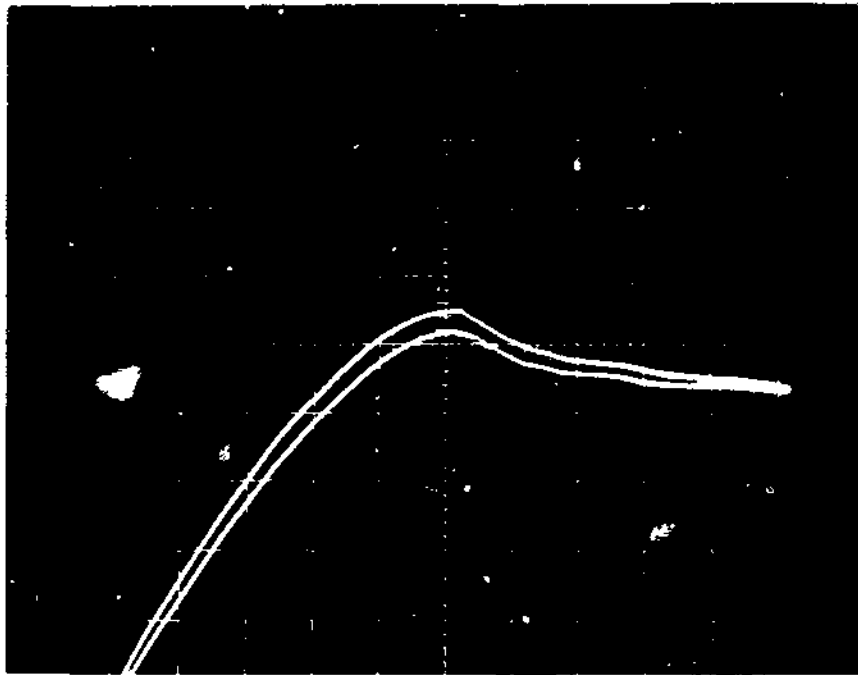
13. J. G. Ruch and G. S. Kino, "Measurement of the Velocity-Field Characteristic of GaAs," Appl. Phys. Letters, vol. 10, pp. 40-42, January 1967.
14. G. S. Hobson, "The External Negative Conductance of Gunn Oscillators," Solid-State Electronics, vol. 12, pp. 711-717, 1969.
15. R. W. H. Engelmann and W. Heinle, "Proposed Gunn-Effect Switch," Electronics Letters, vol. 4, no. 10, pp. 190-192, 17th May 1968.
16. H. Thim, "Stability and Switching in Overcritically Doped Gunn Diodes," Proc. IEEE (Letters), vol. 59, pp. 1285-1286, August 1971.



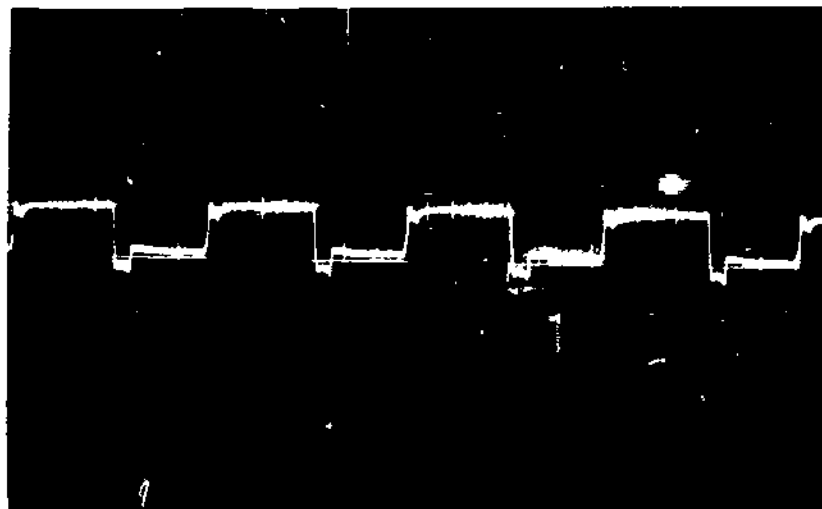
1. Typical CW Gunn Diode Current-Voltage Characteristic and Load Line.



3. Schematic of Experimental Switching Circuit.



2. Diode i-v Characteristic Displayed on Curve Tracer. Vertical Scale: 100 mA/div; Horizontal Scale: 1V/div.



4. Output Voltage Waveform Showing Bistable Operation. Vertical Scale: 5 V/div; Horizontal Scale: 0.5 usec/div.